

REMARKS

This Amendment is being filed in response to the Final Office Action mailed February 17, 2009, and the Advisory Action mailed on April 30, 2009 which have been reviewed and carefully considered. Reconsideration and allowance of the present application in view of the amendments made above and the remarks to follow are respectfully requested.

Claims 1-7, 10-14, 16, 18-20 and 22-25 remain in this application, where claim 25 has been currently added. Claims 1, 5, 10, 14 and 23 are independent.

In the Final Office Action, claims 1, 4, 6, 11-13, 16-17 and 22-24 are rejected under 35 U.S.C. §102(b) over U.S. Patent Application Publication No. 2002/0065810 (Bradley). Claims 2-3, 7, 10 and 18-19 are rejected under 35 U.S.C. §103(a) over Bradley in view of Official Notice. Claim 21 is rejected under 35 U.S.C. §103(a) over Bradley in view of U.S. Patent No. 6,377,500 (Fujimoto). Claim 5 is rejected under 35 U.S.C. §103(a) as allegedly unpatentable over Bradley. Claim 14 is rejected under 35

U.S.C. §103(a) as allegedly unpatentable over Bradley in view of U.S. Patent No. 6,377,958 (Orcutt) and Official Notice. Claim 20 is rejected under 35 U.S.C. §103(a) as allegedly unpatentable over Bradley in view of Official Notice and MRAM as disclosed in <http://en.wikipedia.org/wiki/MRAM>. It is respectfully submitted that claims 1-7, 10-14, 16, 18-20 and 22-24 are patentable over Bradley, Official Notice, Fujimoto, Orcutt and MRAM for at least the following reasons.

On pages 9-10 of the Final Office Action, the Examiner correctly noted that Bradley does not disclose or suggest "to store static data structures of said first file system in a file on said record carrier and volatile data structures of said first file system in said memory means," as recited in claim 21. Column 1, line 40 to column 2, line 15 of Fujimoto is cited in an attempt to remedy the deficiencies in Fujimoto.

Fujimoto is directed to a memory system with a non-volatile memory, having address translating function. An address translation table is stored the table in an area of a RAM that is a volatile memory, where the number of entries in the address

translation table coincides with the number of blocks in a flash memory which is a non-volatile memory. Typically, if the storage capacity of the non-volatile flash memory increases, there must also be an increase in the area of the volatile RAM memory that holds the address translation table. Fujimoto is concerned with not increasing the volatile RAM memory area for the address translation table, even if the storage capacity of a non-volatile flash memory increases.

It is respectfully submitted that the Fujimoto non-volatile flash memory has nothing to do and does not disclose or suggest a record carrier. A record carrier is different from a flash memory and thus presents different considerations. There is simply no disclosure or suggestion in Bradley, Fujimoto, and combination thereof of mapping means which is "adapted to mount said second file system on said record carrier and to translate said second file system in a memory unit into equivalent structures of said first file system, and to store static data structures of said first file system in a file on said record carrier and volatile data structures of said first file system in said memory unit," as

recited in independent claim 1, and similarly recited in independent claims 5, 10, 14 and 23. (Illustrative emphasis provided)

Storing static data structures of the first file system in a file on the record carrier reduces the need to frequent updates, thus reducing the likelihood that the record carrier will run out of cycles for rewriting. No such recyclability issue is present for the Fujimoto non-volatile flash memory. Fujimoto is completely silent about any record carrier, and is oblivious to any recyclability issues. Fujimoto simply does not disclose or suggest "to translate said second file system in a memory unit into equivalent structures of said first file system, and to store static data structures of said first file system in a file on said record carrier and volatile data structures of said first file system in said memory unit," as recited in independent claim 1, and similarly recited in independent claims 5, 10, 14 and 23. (Illustrative emphasis provided)

Assuming, arguendo, that the above noted features of independent claims 1, 5, 10, 14 and 23 are somehow disclosed or

suggested in Bradley and Fujimoto, it is respectfully submitted that Bradley, Fujimoto, and combinations thereof, do not disclose or suggest the present invention as recited in independent claim 1, and similarly recited in independent claims 5, 10, 14 and 23, which, amongst other patentable elements, recites that "said mapping means is further adapted to shift a location of frequently updated data on said record carrier." (Illustrative emphasis provided)

Accordingly, it is respectfully requested that independent claims 1, 5, 10, 14 and 23 be allowed. In addition, it is respectfully submitted that claims 2-4, 6-7, 11-13, 16, 18-20, 22 and 24 should also be allowed at least based on their dependence from independent claim 1 as well as their individually patentable elements. Accordingly, separate consideration of each of the dependent claims is respectfully requested.

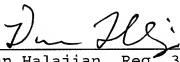
In addition, Applicants deny any statement, position or averment of the Examiner that is not specifically addressed by the foregoing argument and response. Any rejections and/or points of argument not addressed would appear to be moot in view of the

Amendment in Reply to the Final Office Action of February 17, 2009
and the Advisory Action mailed on April 30, 2009

presented remarks. However, the Applicants reserve the right to submit further arguments in support of the above stated position, should that become necessary. No arguments are waived and none of the Examiner's statements are conceded. And in particular, no Official Notices are conceded.

In view of the above, it is respectfully submitted that the present application is in condition for allowance, and a Notice of Allowance is earnestly solicited.

Respectfully submitted,

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